

Ahmed Seddik Mohamed
17TH BUILDING ZAHARAE EL MADDI - CAIRO
Home : 02 - 7547985 - Mobile: 0122 1053653



➤ Personal Information

- ❖ *Birth Date:* 21 October 1954
- ❖ *Marital Status:* Married with three children
- ❖ *Nationality:* Egyptian

➤ Education

- March 2000* Post Doctor study, in “Space Mission Analysis and Design”, SURRY University, England.
- April 1997* Ph.D. Electrical Engineering, in “Automatic Testing of Digital Circuits Using Compact Signature Analysis “, Military Technical College.
- October 1992* M.Sc. Computer Engineering, in “Micro Computer Based Automatic Testing system through Signature analysis” , Military Technical College.
- July 1977* B.Sc .in Electrical Engineering (Radar Specialization) Military Technical College, Cairo Egypt

➤ Work Experience

- Oct 2009-* **Program Manger Chief of Technical Committee for**
- Jan. 2013* **Space and Satellite Technology Transfer Program.**
- Jan 2008* **Egyptian Armament Authority chief assist for**
- Industries and Military Production**
- Jan 2006* **Egyptian Armament Authority chief assist for**
- Research & Development:**
- Director of the military researches (Planning - Managing –*
- Consultant & Evaluation).*

- April 1997- Jan 2006* **R&D department, Egyptian Armament Authority, in**
- charged of the following responsibility:**
- 1-Management and co-ordination of Space & Satellite Technology*
- Transfer Program (SSTTP) for Egyptian Armed Forces.*

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- 2-Establish of Small satellite technical requirement and statement of work.
- 3-Share in negotiation, study, and evaluate of technical proposals from international companies for (SSTTP).
- 4-Define of satellite system tests necessary to demonstrate system quality.
- 5-Define the infrastructure requirement for creating space and satellite technology in Egypt.

Oct 1994–Apr. 1997 **Ph.D. Candidate at the Military Technical College.**

Oct 1992- Oct 1994 **R&D department , Egyptian Armament Authority**
Planning - Managing – Consultant & Evaluation of research projects for electronic systems.

Oct 1992- Oct. 1994 **M.Sc. Candidate at the Military Technical College.**

Jan.1980– Oct.1990 **Chief of AN-TPS - 63 Radar, ARE Operation Center**
Depot repair center.

July1977–Jan.1980 **Repair and maintenance engineer for Radar systems.**

➤ Teaching Experiences

Teaching the following courses at the corresponding Institutes:-

The Military Technical College

Digital Logic Circuits.
Hardware Fault- Tol. of Digital Circuits.
Micro Processors & Microcomputer Applications.
Mathematics.
Computer Architecture.
Automatic Testing Equipment (ATE)& Reliability.

The Technical Institute for Armed Forces

Basic Courses in Math. & Physics.
Radar Maintenance.

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Cairo University (Bani Swif Branche)

Introduction to computers.
Microprocessor & Microcomputer Applications.
Advanced Computer Architecture.

MIU University

Digital Logic Circuits.

➤ **Publications**

- 1- *“Small Low Earth Observation Satellite System architecture and Applications “ , 8th International Conference on Aerospace Science & Aviation Technology (ASAT) , M.T.C , Cairo, Egypt, 1999.*
- 2- *“Improved Compact Signature Analyzer Based On Multiple Input Shift Register(MISR) “ , 7th International Conference on Aerospace Science & Aviation Technology (ASAT) , M.T.C , Cairo, Egypt, 1997.*
- 3- *“Dynamic Behavior of Improved Compact Signature Analysis Based on LFSR “ , 7th International Conference on Aerospace Science & Aviation Technology (ASAT) , M.T.C , Cairo, Egypt, 1997.*
- 4- *“Automatic Testing For Digital Circuit Through Signature Analysis Technique “ , International Association Of Science and Technology For Development, USA, 1994.*
- 5- *“PC- Based Signature Analysis: An Optimization Technique for Fault Detection in Digital Electronic Systems “ , International Conference on Multiple Criteria Decision making , M.T.C , Cairo, Egypt, 1992.*

➤ **Academic Advanced Courses.**

- 1- *Computer Data Structure and DBS.*
- 2- *Computer Simulation Techniques.*
- 3- *Microprocessor Based System .*
- 4- *Microprocessor and Microcomputer Application.*
- 5- *Advanced Computer Architecture.*
- 6- *Fault- Tolerance Design of Digital Circuits.*
- 7- *Theory of Algorithms*
- 8- *Error Correction Codes*

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➤ Professional Background

- 1- Management and co-ordination of Space & Satellite Technology Transfer Program (SSTTP) for Egyptian Armed Forces.*
- 2- Small satellite technical requirement and statement of the work.*
- 3- Leading negotiation, studying, and evaluation of technical proposals from international companies for (SSTTP).*
- 4- Define and implement of satellite system tests necessary to demonstrate system quality.*
- 5- Development of Technology Transfer Programs.*
- 6- Design of Automatic Testing Equipment (ATE) for digital circuits.*
- 7- Depot maintenance, repair, calibration, and refurbishment of electronic systems.*
- 8- Design & carry-out research projects for test stands of different electronic subsystems.*
- 9- Automatic Testing Equipment (ATE), Reliability , and Fault tolerant.*
- 10-Sharing in establishment of quality concerning for electronic industries.*
- 11- Design of Pseudo random generators and compression system using signature analysis technique.*
- 12- Radar Theory, ECM, and ECCM techniques*

➤ Research projects:

Design and implement a series of research projects in the fields of Automatic Testing Equipment (ATE), Satellite Technology, command and control, electronic systems.

➤ Languages

- **Arabic:** Mother Tongue
- **English:** Fluent

➤ Hobbies

Listening to Music, Playing Squash & Camping.